

INTERNATIONAL SEARCH REPORT

Inte 31 Application No
PCT/IB2004/052151

A. CLASSIFICATION OF SUBJECT MATTER
IPC 7 H04L12/56

According to International Patent Classification (IPC) or to both national classification and IPC

B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)
IPC 7 H04L

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic data base consulted during the international search (name of data base and, where practical, search terms used)

EPO-Internal, INSPEC, WPI Data, PAJ

C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category *	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
	-/--	



Further documents are listed in the continuation of box C.



Patent family members are listed in annex.

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Date of the actual completion of the international search

17 March 2005

Date of mailing of the international search report

07/04/2005

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C.(Continuation) DOCUMENTS CONSIDERED TO BE RELEVANT		
Category *	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
X	<p>RIJPKEMA, E.; GOOSSENS, K.; RADULESCU, A.; DIELISSEN, J.; VAN MEERBERGEN, J.; WIELAGE, P.; WATERLANDER, E.: "Trade-offs in the design of a router with both guaranteed and best-effort services for networks on chip" COMPUTERS AND DIGITAL TECHNIQUES, IEE PROCEEDINGS, 'Online! vol. 150, 22 September 2003 (2003-09-22), pages 294-302, XP002321033 Retrieved from the Internet: URL:http://ieeexplore.ieee.org/iel5/2192/2 7911/01245598.pdf?tp=&arnumber=1245598&isn umber=27911&arSt=294-302&ared=&arAuthor=Ri jpkema%2C+E.%3B+Goossens%2C+K.%3B+Radulesc u%2C+A.%3B+Dielissen%2C+J.%3B+van+Meerberg en%2C+J.%3B+Wielage%2C+P.%3B+Waterlander%2 C+E.%3B> 'retrieved on 2005-03-13! figures 3,5,7,8 page 298, right-hand column, line 19 - line 28 page 298, left-hand column, line 47 - right-hand column, line 18; figure 5b page 299, left-hand column, line 19 - line 33 page 299, right-hand column, line 20 - line 21</p>	1-6
A	<p>----- ZEFERINO C A ET AL: "SoCIN: a parametric and scalable network-on-chip" INTEGRATED CIRCUITS AND SYSTEMS DESIGN, 2003. SBCCI 2003. PROCEEDINGS. 16TH SYMPOSIUM ON SEPT. 8-11, 2003, PISCATAWAY, NJ, USA,IEEE, 8 September 2003 (2003-09-08), pages 169-174, XP010658515 ISBN: 0-7695-2009-X page 173, right-hand column, line 5 - line 29; figure 8</p>	5
P,A	<p>----- WO 2004/057808 A (KONINKLIJKE PHILIPS ELECTRONICS N.V.; RIJPKEMA, EDWIN) 8 July 2004 (2004-07-08) page 3, line 28 - page 5, line 23; figure 1a</p> <p>-----</p>	1-6

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Patent document cited in search report	Publication date	Patent family member(s)	Publication date
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